

## Modeling the Drain Current of a PHEMT using the Artificial Neural Networks and a Taylor Series Expansion

*Taj-eddin Elhamadi, Mohamed Boussouis, and Naima Amar Touhami*

Electronic and Instrumentation laboratory,  
Faculty of Science, Abdmalak Essaadi University,  
Tetouan, Morocco

Copyright © 2015 ISSR Journals. This is an open access article distributed under the **Creative Commons Attribution License**, which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

**ABSTRACT:** Artificial neural networks (ANNs) have recently been introduced in the microwave area as a fast and flexible vehicle to microwave modeling, simulation and optimization. The models are fast and can represent EM/physics behaviors it learnt which otherwise are computationally expensive. In this paper a neural network model is developed for a Pseudomorphic High Electron Mobility Transistor PHEMT (ED02AH-6x30), a transistor of 6 gate fingers, each with a width of 30  $\mu\text{m}$ . An multi-layer perceptron (MLP) structure is used to model the nonlinear I-V characteristics, using a data measurement and the back propagation algorithm with adaptive learning rate and momentum for the training process. The final results are compared with data measurement and a good agreement is obtained between model and data. The accurate model will be used finally to obtain a small signal model for the drain current using the Taylor series expansion.

**KEYWORDS:** Neural networks, multi-layer perceptron, back propagation algorithm, PHEMT, Taylor series expansion.

### 1 INTRODUCTION

Microwave transistors (MESFETs, HEMTs, PHEMT and HBTs) are used in majority of devices in modern microwave communication systems (satellite systems, mobile systems, radio-relay, etc.). Development of accurate and reliable models of microwave transistors is one of the basic aspects in design of circuits containing these transistors.

Different equation-based empirical large-signal models have been proposed for the simulation of field effect transistors (FETs) in nonlinear microwave circuits like amplifiers, filters, mixers and multipliers. The majority of empirical models based on empirical measurements to extract the parameters. For GaAs MESFET Curtice, Statz, Materka models are the most used in simulators [1] [2]; many good models have been developed for HEMT and PHEMT devices [3] [4]. However, the existing models with closed-form equations, while good for many existing devices, may not fit well with new devices.

Neural networks, also called artificial neural networks (ANNs), are information processing systems with their design inspired by the studies of the ability of the human brain to learn from observations and to generalize by abstraction. The fact that neural networks can be trained to learn any arbitrary nonlinear input–output relationships from corresponding data has resulted in their use in a number of areas such as pattern recognition, speech processing, control, biomedical engineering etc. Recently, ANNs have been applied to RF and microwave computer-aided design (CAD) problems as well [6] [7] [8] [9].

Artificial neural networks (ANNs) have been in use to replace empirical equations or compact models for a variety of devices such as MESFET, HEMT and PHEMT [10] [11] [12] [13] [14]. These models are based on an equivalent circuit and require less time for their development compared to compact models and offers many advantages:

- Neural networks can “learn” from measured device data, allowing model development, even when formulae are unavailable.
- ANN based model is simple to extract, easy to build and fast to evaluate.

In this paper a neural network model is developed for a Pseudomorphic High Electron Mobility Transistor PHEMT (ED02AH-6x30), a transistor of 6 gate fingers, each with a width of 30  $\mu\text{m}$ . An multi-layer perceptron (MLP) structure is used to model the nonlinear I-V characteristics, using a data measurement and the back propagation algorithm with adaptive learning rate and momentum for the training process. Once the neural network is trained, the drain current can be writing as a mathematical function. The Taylor series coefficients can be extracted by differentiating the ANN mathematical expression as it is n times derivable. The accurate model will be used finally to obtain a small signal model for the drain current.

## 2 MODELING TECHNIQUE

### 2.1 MLP NEURAL NETWORK STRUCTURE

A standard multilayer perceptron (MLP) as shown in fig.1 consists of an input layer (layer 1), an output layer (layer  $N_L$ ) and as well as several of hidden layers [6] [7] [8]. Input vectors are presented to the input layer and fed through the network that then yields the output vector. The lth layer output is:

$$z_i^l = \varphi \left( \sum_{j=0}^{N_{l-1}} w_{ij}^l z_j^{l-1} \right) \quad (1)$$

Where:  $l = 2, 3, \dots, L$  and  $i = 1, 2, 3, \dots, N_l$

Where  $z_i^l$  and  $z_j^{l-1}$  are outputs of lth and (l-1)<sup>th</sup> layer,  $w_{ij}^l$  are the weights between the ith neuron in the lth layer and the j<sup>th</sup> neuron in the (l-1)<sup>th</sup> layer. The function  $\varphi$  is an activation function of each neuron, the linear function is used in output layer, and sigmoid in hidden layers, the sigmoid is defined as:

$$\varphi(x) = \frac{1}{1 + e^{-x}} \quad (2)$$

The neural network learns relationship among sets of input output data (training sets). First, input vectors are presented to the input neurons and output vectors are computed. These output vectors are then compared with desired values and errors are computed. The training process proceeds until errors are lower than the prescribed values or until the maximum number of epochs (epoch is the whole training set processing) is reached. Once trained, the network provides fast response for various input vectors (even for those not included in the training set) without additional optimizations. Usually, neural networks with different number of hidden neurons are trained, tested and after their comparison, the network with the best modeling results is chosen as the neural model [6] [7].

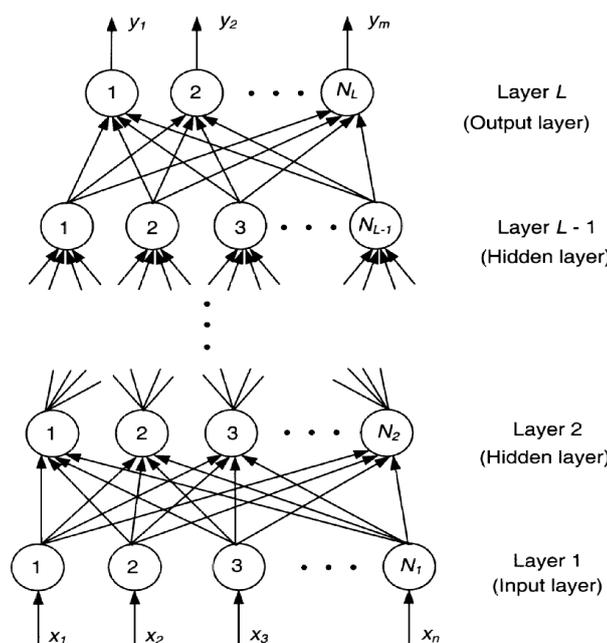


Fig. 1. Multi-layers perceptron (MLP) structure

2.2 THE TAYLOR SERIES EXPANSION OF THE DRAIN CURRENT OF PHEMT

The nonlinear small-signal drain current  $I_d$  depends on both the drain-to-source and gate-to-source bias point  $(V_{d0}, V_{g0})$  and the drain-to source and gate-to-source dynamic voltages over the bias point  $(v_d, v_g)$ . Around a fixed operating point  $(V_{d0}, V_{g0})$  the voltages  $V_d$  and  $V_g$  can be written as:

$$V_d = V_{d0} + v_d \quad (3. a)$$

$$V_g = V_{g0} + v_g \quad (3. b)$$

The drain current  $I_d$  is a function of  $(V_{d0}, V_{g0}, v_d, v_g)$  :

$$I_d = I_{d0} + i_d = f((V_{d0}, V_{g0}, v_d, v_g)) \quad (3. c)$$

If the input excitation is small enough,  $I_d$  can be represented in a small interval around the bias point by the following two dimensional truncated Taylor series expansion:

$$I_d(V_d, V_g) = I_{d0} + G_m v_g + G_d v_d + G_{m2} v_g^2 + G_{md} v_g v_d + G_{d2} v_d^2 + G_{m3} v_g^3 + G_{m2d} v_d^2 v_g + G_{md2} v_g v_d^2 + G_{d3} v_d^3 \quad (3. d)$$

Where  $I_{d0}$  is the DC current and  $G_m, G_d \dots G_{md2}$  are coefficients related to the  $n^{th}$  order derivatives of the I-V characteristic with respect to the instantaneous voltages evaluated at the bias point [15].

$$G_m = \left. \frac{\partial I_d(V_d, V_g)}{\partial V_g} \right|_{V_g=V_{g0}; V_d=V_{d0}} ; G_{m2} = \left. \frac{1}{2} \frac{\partial^2 I_d(V_d, V_g)}{\partial^2 V_g} \right|_{V_g=V_{g0}; V_d=V_{d0}} ; G_{m3} = \left. \frac{1}{6} \frac{\partial^3 I_d(V_d, V_g)}{\partial^3 V_g} \right|_{V_g=V_{g0}; V_d=V_{d0}}$$

$$G_d = \left. \frac{\partial I_d(V_d, V_g)}{\partial V_d} \right|_{V_g=V_{g0}; V_d=V_{d0}} ; G_{d2} = \left. \frac{1}{2} \frac{\partial^2 I_d(V_d, V_g)}{\partial^2 V_d} \right|_{V_g=V_{g0}; V_d=V_{d0}} ; G_{d3} = \left. \frac{1}{6} \frac{\partial^3 I_d(V_d, V_g)}{\partial^3 V_d} \right|_{V_g=V_{g0}; V_d=V_{d0}}$$

$$G_{md} = \left. \frac{\partial^2 I_d(V_d, V_g)}{\partial V_g \partial V_d} \right|_{V_g=V_{g0}; V_d=V_{d0}} ; G_{m2d} = \left. \frac{1}{2} \frac{\partial^3 I_d(V_d, V_g)}{\partial^2 V_g \partial V_d} \right|_{V_g=V_{g0}; V_d=V_{d0}} ; G_{md2} = \left. \frac{1}{2} \frac{\partial^3 I_d(V_d, V_g)}{\partial^2 V_d \partial V_g} \right|_{V_g=V_{g0}; V_d=V_{d0}} \quad (3. e)$$

3 THE PROPOSED STRUCTURE AND TRAINING PROCESS

A multilayer (MLP) Neural network perceptron is used to modeling the current drains  $I_d$ , whose input are  $V_{gs}$  and  $V_{ds}$ . The back propagation algorithm with adaptive learning rate and momentum is used for the training process. After training and evaluation of the networks with different number of hidden neurons, a network with tow hidden layers consisting of 5 neurons for each one give the best results, obtaining a good model requires 361 steps ( $V_{ds}$  (0V to 5V) and  $V_{gs}$  (-2V to 0.6V)). The relative error for each training sample is given in the fig 2 and the table 1 gives performances of the neural model.

Table 1. Neural model performances

Maximum absolute error	Average absolute error	Average relative error
0.57 mA	0.28 mA	0.6%

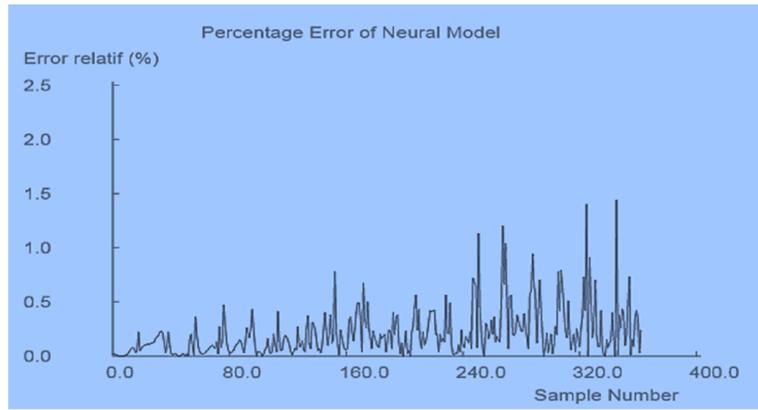


Fig. 2. Relative error for each training sample

#### 4 RESULTS

The model is tested and compared with measurement for different values of  $V_{ds}$  and  $V_{gs}$ . Very good agreement can be observed at fig 3.

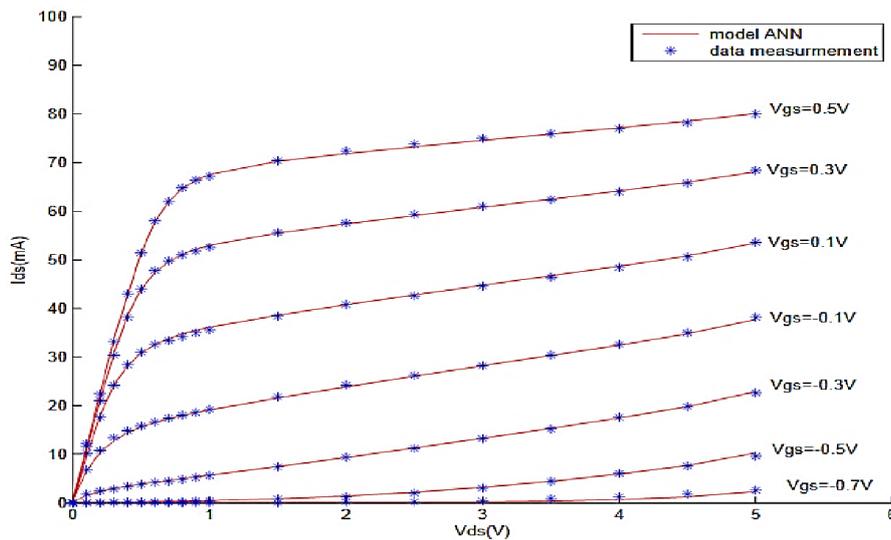


Fig. 3. PHEMT drain-current comparison between ANN model and measurement data.

Once the neural network is trained, the drain current can be written as a mathematical function. The Taylor series coefficients can be extracted by differentiating the ANN mathematical expression as it is  $n$  times derivable. The Figure 4 shows the variation of  $G_m$  and  $G_d$  as a function of  $V_{ds}$  and  $V_{gs}$ . While the results obtained for  $G_m$  and  $G_d$  are compared with values obtained by extraction from measured S-parameters, such as shown in Figure 5.

The Taylor series expansion can be deduced for each bias point and we can predict the behaviors of the drain current source in the small-signal situation.

As an example, for  $(V_{d0} = 2V, V_{g0} = -0.2V)$  we have:

$$I_{d0} = 16.0441mA; G_m = 75.3960mS; G_d = 4.3436mS; G_{m2} = 58.6097mA.V^{-2}; G_{m3} = -71.7296mA.V^{-3}; G_{d2} = -0.1045mA.V^{-2}; G_{d3} = 0.0793mA.V^{-3}; G_{md} = 4.9209mA.V^{-2}; G_{m2d} = -15.355mA.V^{-3}; G_{d2m} = -1.508mA.V^{-3}$$

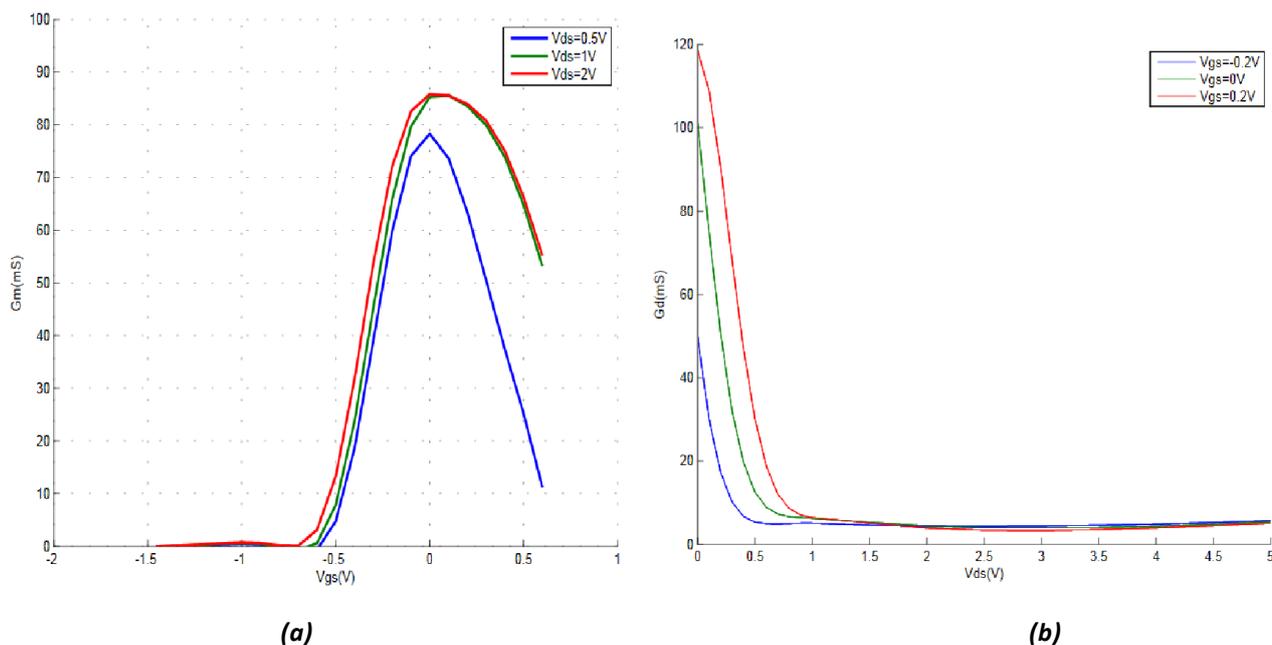


Fig. 4. (a) the transconductance  $G_m$  and (b) the output conductance  $G_d$  provided by the proposed ANN model

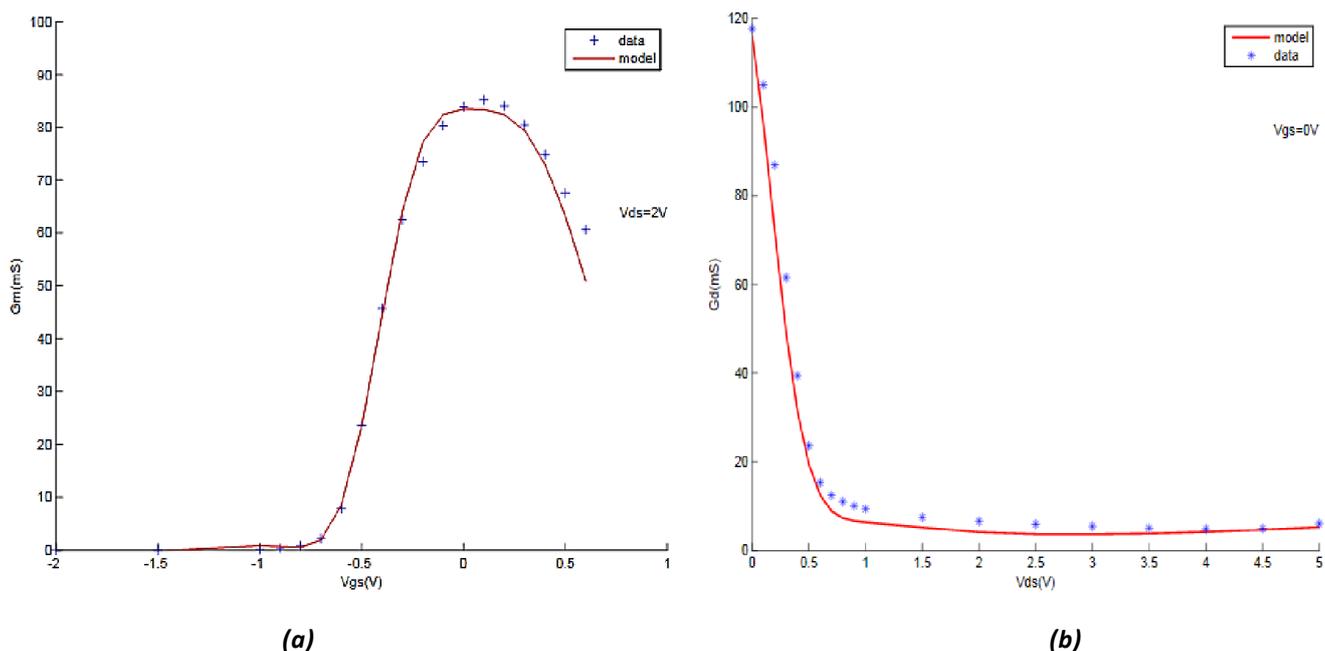


Fig. 5. (a) the transconductance  $G_m$  and (b) the output conductance  $G_d$  compared with values obtained by extraction from measured S-parameters

## 5 CONCLUSION

An MLP structure has proposed for modeling the drain-current of a PHEMT using the back propagation algorithm with adaptive learning rate and momentum for the training process. Good agreement is obtained between model and data measurement. The ANN model mathematical expression is used to obtain the analytical expression of the transconductance, output conductance and all Taylor series coefficients. The behaviors of the drain current source in the small-signal situation are finally modeled by using a Taylor series expansion that can be used in a simulator environment as an accurate and fast model.

## REFERENCES

- [1] W. R. Curtice, «GaAs MESFET modeling and nonlinear CAD,» IEEE Trans. Microwave theory Tech, vol. 36, pp. 220-230, 1988.
- [2] H. Statz, P. Newman, I. W. Smith, R. A. Pucel et H. A. Haus, «GaAs FET device and circuit simulation in SPICE,» IEEE Trans. Electron devices, vol. 34, pp. 160-169, Feb 1987.
- [3] S. J. Mahon, D. J. Skellern et F. Green, «A technique for modelling S-parameters for HEMT structures as a function of gate bias,» IEEE Trans. Microwave Theory Tech, vol. 40, pp. 1430-1440, July 1992.
- [4] M. C. YAGOUB et H. BAUDRAND, Conception de circuits linéaires et non linéaires micro-ondes, Toulouse: CEPADUES, 2000
- [5] B. L. Ooi, Z. Zhong et M. S. Leong, «Analytical Extraction of Extrinsic and Intrinsic FET Parameters,» Microwave Theory and Tech, IEEE trans, vol. 57, pp. 254-261, February 2009.
- [6] S. Samarasinghe, Neural Network for Applied Sciences and Engineering, Auerbach Publications, 2007.
- [7] Q. J. Zhang et K. C. Gupta, Neural network for RF and microwave design, Boston: ARTECH HOUSE, 2000.
- [8] F. Wang, V. K. Devabhaktuni, C. Xi et Q.-J. Zhang, «Neural network structures and training algorithms for RF and microwave applications,» International Journal of RF and Microwave Computer-Aided Engineering, vol. 9, p. 216–240, 1999.
- [9] H. Kabir, Y. Wang, M. Yu et Q. J. Zhang, «Neural Network Inverse Modeling and Applications to Microwave Filter Design,» Microwave Theory and Tech, IEEE Trans, vol. 56, pp.867-879 April 2008.
- [10] Z. Marinkovic, N. Ivkovic, O. Pronic, V. Markovic et A. Caddemi, «Analysis and validation of neural network approach for extraction of small-signal model parameters of microwave transistors,» Microelectronics Reliability, vol. 53, pp. 414-419, 2013.
- [11] M. Hayati et B. Akhlaghi, «An extraction technique for small signal intrinsic parameters of HEMTs based on artificial neural networks,» International Journal of Electronics and Communications (AEÜ), vol. 67, pp. 123-129, 2013.
- [12] X. Li, J. Gao et G. Boeck, «Microwave nonlinear devices modelling by using an artificial neural network» Semiconductor Science and Tech, IOP Publishing, vol. 21, pp. 833-840, 2006.
- [13] H. Ben Hammouda et M. mhiri, «Neural Based Models of Semiconductor Devices for SPICE Simulator» American Journal of Applied Sciences, pp. 385-391, 2008.
- [14] M. H. Weatherspoon, H. A. Martinez, D. Langoni et S. Y. Foo, «Small-Signal Modeling of Microwave MESFETs Using RBF-ANNs,» Microwave Theory and Tech, IEEE Trans, vol. 56, pp.2067-2072 October 2007.
- [15] S. M. Sze et K. N. Kowk, Physics of semiconductor devices, third edition, Wiley, 2007.